

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 07-261861

(43)Date of publication of application : 13.10.1995

(51)Int.Cl.

G05F 1/56

(21)Application number : 06-049515

(71)Applicant : PIONEER ELECTRON CORP

(22)Date of filing : 18.03.1994

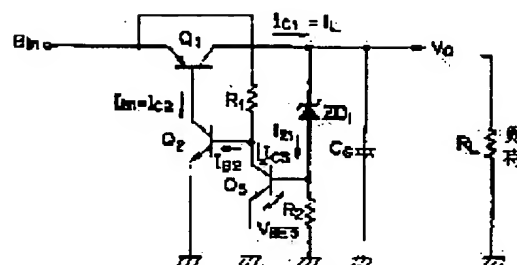
(72)Inventor : YAGAMI HEIHACHIRO
KOBAYASHI HIROSHI
SATO MASANORI

(54) POWER SOURCE STABILIZING CIRCUIT

(57)Abstract:

PURPOSE: To provide a backup power source stabilizing circuit which is low in current consumption.

CONSTITUTION: A control current is supplied to the base terminal of a 2nd transistor(TR) Q2 through a resistance means R1. The 2nd TR Q2 generates a driving current corresponding to a control current and a driving part Q1 generates a load current I2 with the driving current and supplies it to a load RL. As the load current IL rises, an output voltage Vo begins to rise and a detecting means ZD1 generates a detection voltage corresponding to the output voltage Vo. A 1st TR Q3 which inputs the detection voltage at its base terminal has transition from the cutoff state to the conduction state once the detection voltage reaches the base terminal-emitter terminal voltage VBE3. Then the 1st TR Q3 is connected to the base terminal of the 2nd TR Q2, so the current supply by the resistance means R1 is suppressed to suppress the driving current and further the load current IL. This feedback loop holds the constant output voltage Vo at all times.



LEGAL STATUS

[Date of request for examination] 08.09.2000

[Date of sending the examiner's decision of rejection] 12.03.2002

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's
decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office